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PATENT

10716276

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)
Inventor: Peter J. Hopper) Art Unit: 2811
Serial No.: 10/716,276)
Filed: 11/17/2003) Examiner: Donghee Kang
Title: METHOD OF ETCHING A)
LATERAL TRENCH UNDER AN EXTRINSIC)
BASE AND IMPROVED BIPOLAR
TRANSISTOR

Commissioner for Patents
Washington, D.C. 20231

Request for correction of Filing Receipt

Please correct the name of inventor Vladislav Vashchenko to read Vashchenko instead of Vashcherko as shown in the attached copy of the filing receipt.

Respectfully Submitted,

Dated: 1/14/, 2005


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FILE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/716,276	11/17/2003	1763	770	P05731	7	15	2

CONFIRMATION NO. 6046

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FILING RECEIPT



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Date Mailed: 02/19/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Vladislav Vashchenko, Palo Alto, CA;
Peter Johnson, Sunnyvale, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 02/18/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Method of etching a lateral trench under an extrinsic base and improved bipolar transistor

Preliminary Class